

CMOS Analog Integrated Circuits: High-Speed And Power-Efficient Design By Tertulien Ndjountche

By Tertulien Ndjountche

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Complementary metal oxide semiconductor for constructing integrated circuits. CMOS technology is measure of circuit complexity; HCMOS, High-speed CMOS

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Analog Integrated Circuits and Signal Processing, 28, 293 297, 2001 C 2001 Kluwer Academic Publishers. Manufactured in The Netherlands. Circuit Design of a High

A simplified sample and hold circuit diagram. AI is an analog to any significant degree which calls for a very high input Ndjountche, Tertulien

Integrated Circuit TTL High Speed CMOS, Description: The NTE74HC4053 is a digitally controlled analog switch in a 16 Lead DIP type package that utilizes

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I. Tertulien Ndjountche, CMOS Analog Integrated Circuits: High-Speed and Power-Efficient Design (CRC Press, 2011).925 pp. [ISBN: 9781439854914]

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2.2 Floating Effect and its consequences. Although the SOI technology provides a low power, high speed and high device density solution to circuit design, it poses

concepts and techniques required to produce successful designs of analog integrated circuits using CMOS noise High-speed Analog Integrated Circuits

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which are excellent properties for high-frequency analog amplifiers, whereas CMOS technology in integrated circuits, high-speed circuits such as

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High-speed, power-efficient analog integrated circuits can be used as standalone devices or to interface modern digital signal processors and micro-controllers in

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Ndjountche T. CMOS analog integrated circuits: high-speed and power-efficient design. 37 3 Physical Design of MOS Integrated Circuits

increased integration of functional blocks within a single integrated circuit. high-speed CMOS analog enables its use in high-speed,

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